MARKET FOR MEMRISTORS AND DATA MINING MEMORY STRUCTURES FOR PROMISING SMART SYSTEMS *

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Abstract. The article examines the market for promising memristor-based memories for smart systems. The implementation of smart systems is characterized by the widespread use cyber-physical systems, predictive maintenance, AR/VR (Augmented/Virtual Reality) technologies, the Internet of Things (IoT), and Machine Learning algorithms. To stimulate their development, an increasing amount of computational resources and new data storage technologies is required. The current study aims to analyze the development of today’s memristive technologies market in the context of their influence on the development of smart systems. The authors discuss the key stages of the market’s formation and assess the possible effect of memristive technology on various spheres of society’s life. The research results show that the application of memristive technology can affect the development dynamics of both data mining and promising data storage systems. The estimates obtained demonstrate that the memristor market is highly competitive and there are a considerable number of active participants operating on it. The majority of companies expand their market presence by entering various end-user segments. The annual market growth rate will average about 80% and reach an estimate of USD 8.9 billion by 2024 and USD 13.5 billion by 2027.

Keywords: memristor market; industry digitalization; data storage; data mining; smart systems


JEL Classifications: D40, O14, O32, M15

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1. Introduction

The rapid development of digital technology and the widespread adoption of smart systems have emphasized the key role of Big Data management. There are a number of major trends in the digital transformation of industry that are worth mentioning: autonomous robots (Yudin et al., 2017); cloud computing and storage (Che, Safran & Peng, 2013); CAD systems (modeling and simulation) (Demin & Vlasov, 2017); the Internet of Things (Berdyugina, Krivoshein & Vlasov, 2018); and augmented reality (Che, Safran & Peng, 2013). These technologies, when introduced in manufacturing, will lead to the creation of smart factories and initiate a new era in all sectors of industry, which can result in the fourth industrial revolution (The fourth..., 2017), or Industry 4.0 (Witten & Frank, 2016). For this to happen, vast computational resources and storage space are needed.

Computational resources and data storage technology are approaching their maximum capability year after year. Non-volatile CMOS-based memory has already reached its full potential. However, data volumes keep growing, so there is a need to discover a brand-new principle of data storage (Vasilyev & Chernov, 2012). Solutions in this field are characterized by the application of cyber-physical systems (Lee, 2006; Lee & Seshia, 2011), predictive maintenance (Grigoriev et al., 2018), AR/VR (Augmented/Virtual Reality) technologies (Che, Safran & Peng, 2013), the Internet of Things (IoT) (Lee, 2006; Lee & Seshia, 2011), Machine Learning algorithms (Whitaker et al., 2018), and decentralized architecture (Blockchain technology) of Big Data analysis and processing (Che, Safran & Peng, 2013; Muraviev et al., 2019). However, among other solutions, this is a memristor that is considered a promising device in numerous analog and digital applications, especially in memory chips, logic circuits, and neural networks.

The purpose of the paper is to analyze the development of the market for the modern component base of smart systems based on memristor technology that ensure the implementation of intelligent data processing algorithms and methods for their storage.

2. Literature review

Digital transformation involves manipulating not only technical, but also economic, social and other data that are necessary to manage digital production processes in a more effective manner (Litau, 2018a,b). A continuous increase in information volumes has led to the creation of a separate class of data, namely Big Data (Che, Safran & Peng, 2013; Muraviev et al., 2019). To process large volumes, network computing technology has to be used (Veretennikov, 2017), whereas the creation of the memristor component base is believed to be a promising solution in the field of data storage.

A memristor is a non-linear two-terminal electrical component relating electric charge and magnetic flux linkage (Jeong & Shi, 2019). There are various methods for fabricating memristors. The most popular of them are lithography (Jung, 2004), atomic layer deposition (Emelyanov et al., 2015), pulsed laser deposition (Puurunen, 2005) and sputter deposition (Wasa & Hayakawa, 1993). The technological evolution of the memristor-related component base is presented in Fig. 1. The research studies devoted to memristors are gaining in popularity. The number of international scholarly publications increased from 2 pieces in 2005 to 48 pieces in 2015. The number of international patent applications is also rising – from 40 in 2005 to 158 in 2015 (Borodina et al., 2017).
When analyzing the development trends in memristor technologies, it is worth noting their high versatility (Ho, Huang & Li, 2009). In terms of memory chips, memristors can be applied in cell structures of resistive random-access memory (RRAM) and in memristor-based content addressable memories (MCAM) using a combination of memristor and memory cell technology, as well as in multilevel memristor memory (MLMM) (Yener & Kuntman, 2012). Due to the powerful capabilities of memristor technology, Ho and Huang (2009) suggested using these advantages for making calculations directly inside the system, eliminating the need to upload data to the cloud and thus increasing the security of such systems. Memristor-based logic has a remarkable ability that allows arranging memory cells on a single chip. Memristors can be applied in the development of switching units in field-programmable gate array (FPGA) (Sampath, Mane & Ramesha, 2015).

There are three main directions for the research of memristors: developing various types of memristors, searching for memristor manufacturing options, and dealing with the problem of manufacturing memristors (Sherief et al., 2019) (Fig. 2).

Currently, the memristor market’s investment attractiveness is at its early stage of development as there is lack of devices ready for mass production (TMR, 2017). The formation of the memristive technology market is affected by a basic operational contradiction (Fig. 3): if a material of better quality and of higher price is used to fabricate memristors, the quality of the memory created is enhanced in terms of the number of writes and response time;
however, it causes a serious rise in production costs and makes it difficult to start mass production. The choice of a memristor material determines the “memory strength” reflected in the number of rewrite cycles and the total cost of production.

![Operational controversy scheme]

**Fig. 3.** Operational controversy scheme

Memory devices are increasingly becoming a bottleneck in data storage and data retrieval processes, and this limits storage performance. To improve computing speed significantly, scientists are striving to design smaller and denser memory devices that operate at high speed and consume low power (RM, 2018). With the advent of advanced technologies, data storage systems process more information than ever before. To do so, they should be of high performance, accessible, scalable, and manageable (Shukla & Sharma, 2017). The main studied parameters of memristor technologies and their influence are shown in Table 1.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Effect</th>
</tr>
</thead>
<tbody>
<tr>
<td>Storage density</td>
<td>Capacity increase (Vasilyev &amp; Chernov, 2012)</td>
</tr>
<tr>
<td>Response time</td>
<td>Increased data processing speed (Makarimov, 2018)</td>
</tr>
<tr>
<td>Synaptic properties</td>
<td>Empowering Artificial Intelligence (Snider et al., 2011)</td>
</tr>
<tr>
<td>Power efficiency</td>
<td>Computing systems autonomy (Ho, Huang &amp; Li, 2009), power capacity (MM, 2017)</td>
</tr>
</tbody>
</table>

The forecast properties of memristors and the current memories are compared in Table 2 (Sherief et al., 2019; TMR, 2017; RM, 2018; Shukla & Sharma, 2017).
Table 2. Memory-related memristor properties

<table>
<thead>
<tr>
<th>Property</th>
<th>Memristor</th>
<th>PCM</th>
<th>STTRAM</th>
<th>DRAM</th>
<th>Flash</th>
<th>HDD</th>
</tr>
</thead>
<tbody>
<tr>
<td>Storage density (F²)</td>
<td>&lt;4</td>
<td>8–16</td>
<td>57–64</td>
<td>8–8</td>
<td>4–6</td>
<td>2/3</td>
</tr>
<tr>
<td>Energy per bit (pJ)</td>
<td>0.1–3.0</td>
<td>2–27</td>
<td>0.1</td>
<td>2</td>
<td>10000</td>
<td>1–10x10⁸</td>
</tr>
<tr>
<td>Read timing (ns)</td>
<td>10–100</td>
<td>20–70</td>
<td>10–30</td>
<td>10–50</td>
<td>25000</td>
<td>5–8x10⁶</td>
</tr>
<tr>
<td>Write timing (ns)</td>
<td>~10</td>
<td>50–500</td>
<td>13–95</td>
<td>10–50</td>
<td>200000</td>
<td>5–8x10⁶</td>
</tr>
<tr>
<td>Retention</td>
<td>Years</td>
<td>Years</td>
<td>Weeks</td>
<td>&lt;Seconds</td>
<td>Years</td>
<td>Years</td>
</tr>
<tr>
<td>Rewrite cycle</td>
<td>&gt;10¹²</td>
<td>10⁷</td>
<td>10¹⁵</td>
<td>10¹⁵</td>
<td>10⁹</td>
<td>10⁴</td>
</tr>
</tbody>
</table>

Based on recent studies, it can be concluded that the use of memristor technology will affect the dynamics of the development of both data mining and promising data storage systems (Vasilyev & Chernov, 2012; Vera-Tasama, Gomez-Cano & Marin-Hurtado, 2019; Marani, Gelao & Perri, 2015).

3. Methods

Designing an approach to the problem of analyzing the dynamics of the memristor market development includes the formulation of the theoretical framework of the research, analytical models, search queries, hypotheses, as well as the identification of the factors that can affect the nature of the study.

Analytical research methods are diverse (Fig. 4).
The expert approach, upon which the current study is built, should be discussed in more detail. Methods of expert assessments are methods of organizing work with experts and processing opinions of experts expressed in a quantitative and (or) qualitative form.

The research procedure suggests direct communication of expert opinions.

When analyzing expert assessments, a variety of statistical methods can be utilized (Table 3): consistency check (or expert classification, if no consistency is observed) and averaging expert opinions within an agreed group. It is a widespread approach to use simultaneously the mean ranks method and the median ranks method.
Table 3. Methods and algorithms for analyzing expert assessments

<table>
<thead>
<tr>
<th>Method</th>
<th>Algorithm</th>
</tr>
</thead>
<tbody>
<tr>
<td>Mean ranks method</td>
<td>Calculation of the sum of ranks assigned by experts</td>
</tr>
<tr>
<td></td>
<td>Dividing the amount by the number of experts</td>
</tr>
<tr>
<td></td>
<td>Final ranking for calculated average ranks</td>
</tr>
<tr>
<td>Median ranks method</td>
<td>Sort expert scores for each item in non-decreasing order</td>
</tr>
<tr>
<td></td>
<td>Choose a median (centrally ranked)</td>
</tr>
<tr>
<td></td>
<td>Final median ranking</td>
</tr>
<tr>
<td>Clustered ranking matching</td>
<td>Selection of conflicting pairs of objects</td>
</tr>
<tr>
<td>method</td>
<td>Formation of clusters of the final clustered ranking</td>
</tr>
<tr>
<td></td>
<td>Cluster ordering</td>
</tr>
</tbody>
</table>

Applying the methods from Table 3, we have assessed the dynamics of the memristor market development by analyzing expert panels, as well as summarizing the data from statistical reports (MMR, 2019; MW, 2020; Kastalskiy, 2017; Berd, 2014; Pechatnick, 2010). Quantitatively the market is assessed according to basic indicators. The level of concentration of the industrial market is one of the key characteristics of its structure. Other characteristics of the market are taken into account along with concentration. The structure of the industry market is revealed by three basic indicators – the number of suppliers, the suppliers’ market share, and the market concentration indicators (Kuzmin, Volkova & Fomina, 2019).

The qualitative assessment of the representativeness of the sample for identifying the dynamics of the memristor market development indicators was based on the results of several studies that concentrated on exploring the

4. Results

Key factors that navigate the growth in the global memristor market size are advantages of memristors over other memories, increase in number of industrial robots, which require larger memory, increase in demand of tablets, watches, smart phones and other smart wearable devices, whose power capacity can significantly enhance due to memristors (MMR, 2019). According to Cisco Systems’ estimates, by 2022 the number of wearable devices in the world can reach 1,105 billion units as compared to 593 million units in 2018 (Fig. 5).

![Fig. 5. The use of wearable devices with a forecast up to 2022 by region](source: Statista, 2020).

The growing popularity of the IoT and increasing demand for neural networks open up new opportunities for participants in the memristor technology market. In 2018, the memristors market was valued at USD 278.05 million (PRN, 2019). Estimates of the market are increasing every year. Numerous forecasts indicate that the annual market growth rate will average about 80% and reach an estimate of USD 8.9 billion by 2024 and USD 13.5 billion by 2027, and there is still more to come (MI, 2020; FMI, 2016; IA, 2020).

The interest of researchers in memristors is constantly growing. Figure 6 shows the increase in the number of publications on memristors: by 2020, their number has increased 8 times. Figure 7 illustrates an increase in the
number of memristor-related articles (according to the publication archive of Web of Science, Scopus and eLIBRARY.RU). The rise is even more significant here.

![Fig. 6. The number of publications on memristor technology](image)

*Source:* (Sherief et al., 2019; TMR, 2017; RM, 2018; Shukla & Sharma, 2017).

![Fig. 7. Cumulative publication per year](image)

*Source:* (Sherief et al., 2019; TMR, 2017; RM, 2018; Shukla & Sharma, 2017).

The memristor market is highly competitive. Figure 8 shows that the key players and investors of the market are the following: HP development company, L.P. (www.8.hp.com); Intel Corporation (www.intel.ru); Knowm Inc (known.org); Micron Technology, Inc. (www.micron.com); Panasonic Corporation (www.panasonic.com); Rambus Incorporated (www.rambus.com); Samsung (www.samsung.com); San Disk Corporation (www.shop.westerndigital.com/sandisk); Sk Hynix Inc. (www.skhynix.com); and Toshiba Corporation (www.toshiba.co.jp). Most companies expand their market presence by entering various end-user segments. The effectiveness of such a strategy is grounded by forceful arguments (Toomsalu et al., 2019; Chernova et al., 2019).
According to Accurize Market Research (AMR, 2016), the global memristor market is geographically categorized into North America – 33%, Europe – 27%, Asia Pacific – 24%, rest of the world – 16%. North America is one of the most significant markets for memristors (Fig. 9). This is due to the high level of investment in research and development from local market players, many of which are located in the United States (Lambert, 2020). The country is also among the key participants in most memristor applications, such as neuromorphic computing, car electronics, flexible electronics, the IoT, and industrial robotics. The region’s investments in edge computing and systems on a chip (SoC) are significantly higher compared to investments from companies from other regions of the world.
When analyzing global trends in the development of memristive technology, it can be stated that they are passing through the second stage of development (Fig. 10). The first stage of this technology’s development is over. Its major result is the discovery of the possibilities of creating memristors. At the moment, global memristor manufacturers are actively exploring the possibility of improving the current results to pass through the second stage and enter the third one.

As with the development of processors, memristive technology will improve its capacity in the process of its further introduction and integration. In this regard, we can assert that memristors will be going through at least 4 development stages starting from single-thread systems to multi-thread ones that will take the form of a memristor processors network (Potkina & Kholopova, 2014). With the emergence of first memristor processors capable of integrating into common computing systems, memristive technology will start being introduced and applied across all countries.

However, despite all the existing inventions, the introduction of this technology can be projected for the next decade only, since there are still a vast number of unresolved technological and economic issues, in particular, the need for substantial investment in re-equipment. Based on the analysis of the current situation using the data from patent reports and publication activity (Kastalskiy, 2017; Berd, 2014; Pechatnick, 2010; Politbook, 2020), it is possible to forecast future change in the memristive technologies market (Fig. 11).
It can be assumed that the first breakthrough in memristive technology will happen in the field of data storage. A fundamentally new type of memory and its properties allow storing much larger amounts of data, while production costs are comparable to current high-budget solutions.

5. Discussion

The effect of memristive technology on the development trends in modern smart systems is presented in Fig. 12. Memristive technology is versatile. In terms of manufacturing and IT, this technology will lend powerful impetus to the development of computer technology, forecasting and automation algorithms. In the field of social technology, the introduction of memristors can significantly improve the security of personal data (Halkos & Skouloudis, 2016; Metaxas, 2016). Implementing memristor technology in medicine will generate abundant resources for scientific research in the field of vaccines and diseases. Robotics will acquire genuine artificial intelligence that can simulate human behavior without using absolute algorithms, the variability of which is limited only by positive or negative values.
The benefits of implementing memristive technologies, such as increased capacity and energy efficiency, will contribute to the development of Big Data. This will entail the expansion of CAD systems’ capabilities while simulating the environment through creating digital twins (Vichugova, 2020).

The efficiency of the IoT is expected to rise, which will make it possible to decentralize increasingly complex mechanisms, and sensors will be able to track and transmit larger volumes of data. All devices can be synchronized within the same ecosystem and work as a single unit. Cloud storage and computing will become the crucial link between specialists in different fields, simplifying the introduction of changes and adjustments to the development and production process.

The similarity between the memristor and the synapse will make the next step forward in the field of neural networks and artificial intelligence. Neural computing productivity expanded massively will lead to even more
efficient machine learning, and computer “vision” will move far forward, which can significantly accelerate the development of robotics. Due to memristive technologies, artificial intelligence and synaptic neural networks will be able to get closer to the power, flexibility and variability of human brain. All the aspects mentioned above are a prerequisite for further development of smart systems.

Conclusions

The present paper has concentrated on the analysis of memristive technology. Having considered the available data, one can conclude that today’s market of memristor-based devices is, in the first place, a market of ideas, research studies and patents. The research interest in memristors is constantly growing. Over the last decade, the number of publications on memristors has increased 8 times. The similar dynamics is observed among studies indexed in Web of Science, Scopus and eLIBRARY.RU. The obtained estimates show that the memristor market is highly competitive and is represented by a number of active players. The majority of companies expand their market presence by entering various end-user segments. The annual market growth rate will average about 80% and near USD 8.9 billion by 2024 and USD 13.5 billion by 2027. North America is among the strongest markets for memristors. This is due to significant investment in R&D from local market players, many of which are situated in the United States. However, in terms of the memristor technology implementation, it is only possible to make forecasts for the next 10 years, since many unsolved technological and economic problems still persist.

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